

**Amendment to the Specification**

Please attach the following Abstract to the Specification

**Abstract**

A circuit for distributing a clock signal in an integrated circuit includes a capacitive clock distribution circuit. At least one additional inductor is formed in a metal layer of the integrated circuit and is coupled to the clock distribution circuit. The inductor, which may be distributed throughout the integrated circuit, has an inductance value selected to resonate with the impedance of the capacitive clock distribution circuit. By operating the clock distribution circuit at resonance, power dissipation is reduced while skew and jitter performance can be improved.